

Lab 04

```

    a0
    if (a > 0 && a < 10) {
        x = 1
    } else {
        x = 2
    }

```

cond :

```

    li t0, 10
    ble a0, 0, else
    bge a0, t0, else
    li t1, 1
    j end

```

else:

```

    li t1, 2

```

```

    end: mv a0, t1
        ret

```

Endianness - Byte ordering



